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PERFORMANCE ANALYSIS OF BAND PASS FILTER DESIGN TECHNOLOGIES

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ABSTRACT

This research work focuses on different design techniques of bandpass filter and their results are compared. Improvement of filter performance and reduction in components size is became prime concern with rapid advancement of technologies. In this paper, Symmetrically Triangular Open Loop Resonator, Self-Coupled Folded-Line Resonator, Dual-Stub-Loaded Spiral Resonators and Stepped Impedance Resonators.

INTRODUCTION

Band Pass Filters can be used to isolate or filter out certain frequencies that lie within a particular band or range of frequencies. The cut-off frequency or fc point in a simple RC passive filter can be accurately controlled using just a single resistor in series with a non-polarized capacitor, and depending upon which way around they are connected, a simple frequency selective filter that allows a selective range of frequencies are Bandpass filter (BPF).

Microwave filters with good frequency selectivity, wide stop-band, compact size, and low insertion loss are very much desirable in the radar and communication field.Bandpass filters are compact with various filters to allow sort of desirable outcomes to perform inline communication. In order to achieve the performance criterion various methods have been proposed. Some methods are used for better communication and channel forecasting, a widely useable mode of communication is based on Pass-Band and stop-band. Placement of finite transmission zeros (FTZ) plays an important role in the designing of filter, and frequency response is also affected by the FTZs locations. FTZs located symmetrically on the real axis on the complex frequency plane give better out of band frequency selectivity. FTZs on the real axis the used for linear phase response in pass-band whilean imaginary axis is used for better stop-band performance.Bandpass filter has a center frequency of passband with half wavelength and its frequency of resonant is differing with three-time of the fundamental frequency. The suppression of the frequency cause more noise with the signal communication with satellite communication mode and channel. In this paper we studied some of known and useful techniques that give a concern of the techniques that are already established and well known with implemented filters

LITERATURE SURVEY

Cheng-Guang Sun [1], Microstripbandpass channel is a significant part in present day remote correspondence frameworks. The advancement of channels created in the printed circuit board has been given a lot of consideration to miniaturization. To fulfill the prerequisites, an assortment of minimal bandpass channels has been considered [2-4]. Famously, a half-wavelength open loop resonator, and in excess of two resonator units are embraced [5]. To acquire high-recurrence selectivity or harmonic suppression, a square open circle resonator is utilized in the bandpass channel structure [6]. This channel is likewise with little in size. Utilizing triangular open circle resonator, a microstripbandpass channel have been created, it is displayed great separating exhibitions. Be that as it may, the littler



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size and sharp dismissal are quite required for the bandpass channel structure.

Zhang JuHou [7], The structure of BPFs is one of the most troublesome difficulties with some tradeoffs, for example, insertion misfortune, operation bandwidth, stopband suppression and chip size. Regarding on-chip inactive gadget plans, there are a few plan approaches including altered transmission lines [9], hybrid lumped and conveyed parts [10], edge-coupled [11] and broadside-coupled structures [8] to lessen the chip size of BPFs while maintaining a general required exhibition. To further minimize the chip size, a progression of works have been accounted for by methods for in-band flatness improvement [9], stopband suppression upgrade [12] and chip size miniaturization [8]. In spite of the fact that the BPF based edge-coupled resonator [11] has a low insertion misfortune, its chip size is enormous because of the planar nature of the edge-coupled resonator. Along these lines, it is attractive that both chip size and insertion misfortune are considered, at the same time, for best execution.

Kai-da XU [13], A harmonic-stifled BPF dependent on a triple-mode stub-stacked resonator is proposed in [14], which has the preferred position that the even-mode frequencies can be deftly controlled though the odd-mode frequencies are fixed. Be that as it may, it involves complex structure in the plan procedure. In [15], a triple-mode BPF using a changed roundabout fix resonator is introduced, however the terrible dismissal in the stopband happens. Reference [16] presents a wideband BPF with controllable bandwidth and suppression of the harmonic band, wherein four same open circle resonators are received. Be that as it may, the recurrence selectivity still should be improved. In [17], a novel deserted open-circle resonator as the slot-line configuration is applied to structure a minimal triple-mode surrendered ground waveguide resonator-based BPF, which is smaller and simple for integration with planar innovation. In addition, a novel triple-mode hexagonal BPF with capacitive loading stubs is introduced in [18], which is created from an ordinary hexagonal circle double mode resonator. In [19], a triple mode channel using a winding resonator stacked with two short-stubs and an open-stub is displayed.

J. Martel [20] A new type of miniaturized stepped impedance resonator (SIR) for bandpass filter applications is proposed in this paper. The new resonator incorporates a ground plane window with a floating conductor in the backside of the substrate. The ground plane window increase the characteristic

impedance of the lines used to implement the inductive region of the quasi-lumped resonator, thus allowing some size reduction. Moreover, the presence of a floating conducting patch printed below the capacitive region of the resonator pushes up the first spurious band of the filter. A meaningful improvement of its out-of-band rejection level is then achieved. The coupling between adjacent resonators is also enhanced thus leading to wider achievable.

BANDPASS FILTER DESIGING **TECHNIQUES**

A triangular open loopresonator, as shown in Fig. 1, consists of an isosceles righttriangular open loop with connecting to two feed lines directly, and it is with a small size usefully. It resonates around 1.64GHz in frequency as given size in the figure. In order to obtain the lower frequency while holding the smaller in size above the resonator must be improved. From the figure, we foundsome space inside the loop can be used. Therefore, anadditional open loop is embedded into the space.

Thecapacitive loaded lossless transmission line resonator can be described as in Fig. 3, where C_L is the loaded capacitance; Z_a , β_a , and d are the characteristic impedance, the propagationconstant, and the length of the unloaded line, respectively. Theequations are as follows [5]:

onows [3].

$$\theta_{a0} = 2 \tan^{-1} \left(\frac{1}{\pi f_0 Z_a C_L} \right)$$
(1)

$$\theta_{a1} = 2\pi - 2 \tan^{-1} (\pi f_1 Z_a C_L)$$
(2)

From equations above, $\theta_{a0} = \pi$ and $1\theta_{a0} = 2\pi$ when CL = 0. This is for the unloaded half-wavelength resonator. If $C_L = 0$, the resonant frequency reduces as the loading capacitance increases.



Fig. 1 A triangular open loop resonator

The ratio of the first spurious resonant frequency to the fundamental frequency, f1/f0 increases with the loadingcapacitance, resulting in wide stop-band. At the same time, sizereduction has been obtained.



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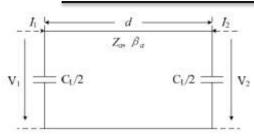


Fig. 2 Capacitive loaded transmission line resonatorequivalent circuit

compact and low-loss BPF using a self-coupled folded-line resonator (SCFLR) provides not only flexible self-resonant-frequency (SRF) to determine the transmission zero (TZ) but also relatively low insertion loss with an ultra-compact size.SCFLR in different layers, as well as in a 3-D view, is shown in Figs. 3(a) and (b), respectively.

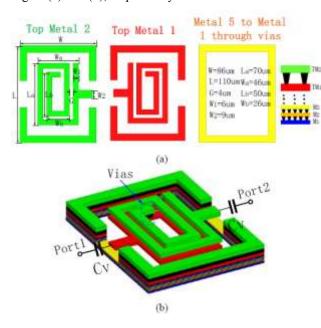


Fig.3 (a) Different layers of the SCFLR (b) 3D view BPF

In this design, a standard 0.13-µm (Bi)-CMOS technology is utilized to implement the BPF by 7 metal layers including two top metal layers with high conductivity. The process-design-kit (PDK) offers metal-insulator-metal (MIM) capacitors, which are sandwiched between Top-Metal 1 (TM_1) and Metal 5 (M_5) . Moreover, the dielectric constant of SiO2 is 4.1 and the loss tangent is 0.01. The height of the silicon substrate is 200 µm. As illustrated in Fig. 1(a), the

Top-Metal 2 (TM_2) and TM_1 layers are mainly used to implement the top and bottom spiral inductors.

To investigate the insight of the presented SCFLR in Fig. 3(a), an L-C equivalent model is presented in Fig. 4(a). The TM_2 inductor L_{TM2} is connected to TM_1 inductor L_{TM1} by vias. To analyze the SCFLR, the equivalent L-C model is simplified with a pair of inverse-coupled inductors LC and a parasitic capacitor *CP* as shown in Fig. 4(b).

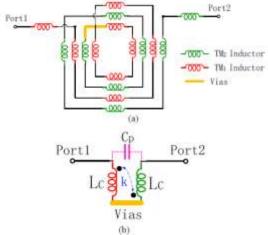


Fig. 4 SCFLR model (a) L-C equivalent circuit (b) simplified circuit

Equivalent inductor L_E of the inverse-coupled inductors could be expressed as follows:

$$L_E = 2(1+k)L_C$$
 (3)

Here, the equivalent inductor L_E and the parasitic capacitor C_P illustrate a simplified L-C equivalent circuit of the SCFLR, which could determine the transmission zero by means of a tunable SRF.

The transmission zero is found to satisfy the following equation:

$$f_{tz} = \frac{1}{2\pi\sqrt{2(1+k)L_C L_P}} (4)$$

The schematic layouts of dual-T-stub loadedspiral resonators are shown in Fig. 5, which sharethe same equivalent structure. Because of the spiral structures, when the electrical lengths are fixed, the sizes ofthese two proposed resonators can be reduced greatly.



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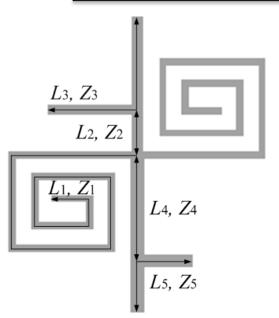


Fig. 5 Centrally Symmetrical Spiral Resonator

Fig. 6(a) illustrates the equivalent configuration of the spiral resonators, which is composed of a uniform microstrip half-wavelength resonator and a pair of T-shaped stubs with different lengths. θ irefers to the electrical length of Li, and Zi denotes the characteristic impedance of the corresponding strips (i = 1, 2, 3, 4 and 5). As the equivalent configuration is symmetrical instructure, apparently, we could utilize the odd- and even mode method to further analyze this configuration. For the even-mode excitation, an approximately equivalent circuits depicted in Fig. 6(b). We simplify the analysis by setting Z1 = Z3 = 2Z2 = 2Z4 = Z5, therefore, the input impedances of the two even-mode equivalent circuits Zin even 1 and Zin even 2 can be deduced as follows:

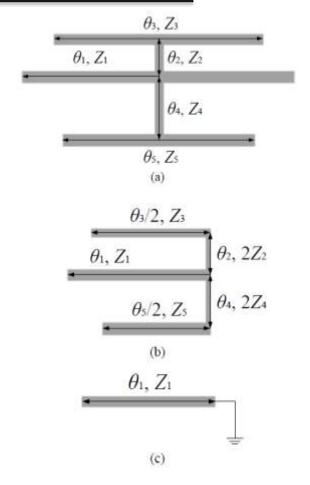


Fig. 6.Equivalent configuration and equivalent circuits of theproposed spiral resonators: (a) Equivalent configuration.(b) Even-mode equivalent circuit. (c) Odd-modeequivalent circuit.

$$Z_{in_even_1} = \frac{Z_1}{j \tan(\theta_1 + \theta_2 + \theta_3 / 2)},$$

$$Z_{\text{in_even_2}} = \frac{Z_1}{\text{j} \tan(\theta_1 + \theta_4 + \theta_5 / 2)}$$

Because of the resonance conditions that $Im(Zin_even_1) = \Box$ and $Im(Zin_even_2) = \Box$, we can derive the following results:



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$$f_{\text{even}_1} = \frac{nc}{2(L_1 + L_2 + L_3 / 2)\sqrt{\varepsilon_{\text{eff}}}}$$

$$f_{\text{even}_2} = \frac{nc}{2(L_1 + L_4 + L_5 / 2)\sqrt{\varepsilon_{\text{eff}}}}$$

where n=1, 2, 3..., c is the velocity of light in free space, and ε eff denotes the effective dielectric constant of the substrate. For the odd-mode excitation, its equivalent circuit is depicted in Fig. 4(c). The input impedance of the odd-mode circuit \mathbb{Z} in_odd can be obtained as follows:

$$Z_{\text{in odd}} = jZ_1 \tan \theta_1$$

The resonance condition is $Im(Zin_odd) = \Box$. Therefore, when the odd mode is excited, the resonant frequency canbe deduced as:

$$f_{\text{odd}} = \frac{(2n-1)c}{4L_1\sqrt{\varepsilon_{\text{eff}}}} \cdot$$

When L2 + L3/2 < L1 < L4 + L5/2, it can be derived that $feven_2 < fodd < feven_1$. Following this analysis, a triple-moderesonator is presented. The above analysis shows that theresonant frequencies of two proposed triple-mode resonators could be changed by tuning the lengths of L1, L2, L3, L4, and L5.

A substrate integrated waveguide (SIW) filter for satellite ground terminal is proposed which also shows improvement in the stop band performance. Waveguide components are used widely in the microwave frequency range due to their high Q values and high power capability but they are bulky, costly and not suited for high density integration. Here substrate integrated waveguide overcome all these difficulties arises with waveguide. Substrate integrated waveguide provides low weight, low cost, low profile with high performance maintenance. Substrate integrated waveguide is a dielectric filled waveguide consist of metallic cylinders also called vias used to connect or unite upper metallic plate with ground. Another advantage of SIW is that it can be fabricated on the PCB byusing standard printed circuit board fabrication technique and system-on package [21]. SIW is a potential as well as promising technology for microwave designing. Planar technology (such as microstrip, strip line and coplanar waveguide) of microwave component designing having high transmission losses while SIW provides better electrical shielding apart from that SIW has viability to incorporate active and passive components on same substrate.

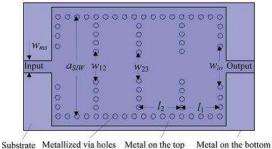


Fig. 7SIW Filter for satellite ground terminal [21]

CONCLUSION

There are several methods are available to design a filter, In this paper some of methods are discussed and compared along with the results and advantages. Compatibility of methods may differ according to the desired filter specification required for the particular use. Discussed filtering techniques led the evolution of number of filters. These filters have been used in wide range of applications.

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