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SPEED CONTROL &POWER FACTOR CORRECTION OF BLDC MOTOR DRIVE FED BY ZETA CONVERTER

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ABSTRACT
This paper proposes a brushless dc (BLDC) motor fed by zeta converter for power factor correction (PFC). A single phase supply followed by an uncontrolled bridge rectifier and a Zeta DC-DC converter is used to control the voltage of a DC link capacitor .The PFC-based zeta converter is designed to operate in discontinuous inductor current mode thus utilizing a voltage follower approach which requires a single voltage sensor for DC link voltage control and PFC operation. The speed variation in the BLDC motor is achieved by varying the DC link voltage. The DC link voltage is calculated corresponding to the duty cycle of the converter switch. The proposed drive with zeta converter is realized as an efficient method for power factor correction in low-power applications also. A MATLAB/Simulink is used to simulate the developed model to achieve the range of speed control with high power factor and improved Power Quality at the supply.

KEYWORDS: *Brushless DC motor (BLDCM), DC link voltage, PID controller, PFC (Power Factor Correction), Zeta converter.*

1. INTRODUCTION

Brushless DC motor (BLDCM) is well suited in large number of applications. Due to it's higher in efficiency, reliability, and torque and lower in noise level, maintenance requirements, electromagnetic interference (EMI) and of long life time. It is also named as electronically commutated motor. Since it has no brushes and commutator, rather an electronic commutation through VSI is used with switching sequence of VSI's switches depending up on the rotor position as sensed by Hall Effect position sensors. This motor is a trapezoidal back-EMF motor which has permanent magnets on the rotor and three phase windings on the stator. Brushed DC motor is increasingly being used in military, industrial and commercial applications. To provide a regulated output dc voltage in power systems, a DC/DC converter is being used. There are four main types of converters, usually called the Buck, Boost, Buck-Boost and cuk converter.CUK, SEPIC and Zeta converters are the new classes of DC-DC converters. Among them, Zeta converter is the proposed in this paper. The proposed PFC-based zeta converter feeds a BLDC motor drive. The speed of BLDC motor is being controlled by varying the dc-link voltage of VSI. The PFC zeta converter acts as an inherent PF corrector as it is designed to operate in DICM. A Single voltage sensor is used to realize the complete operation of BLDCM. To reduce the switching losses an electronic commutation of BLDC motor is used. [1]

2. MATHEMATICAL MODEL OF BLDC MOTOR DRIVE SYSTEM

A 3 phase - Y connected trapezoidal back-EMF type BLDC is modeled. Trapezoidal back-EMF is referring that mutual inductance between stator and rotor has trapezoidal shape. Therefore a b c phase variable model is more applicable than d-q axis. With the intention of simplifying equations and overall model the following assumptions are made:

- Magnetic circuit saturation is ignored.
- Stator resistance, self and mutual inductance of all phases are equal and constant.
- Hysteresis and eddy current losses are eliminated.
- All semiconductor switches are ideal.

The electrical and mechanical mathematical equations of BLDC are:

Voltage equations of each phase of BLDC motor

$$= + (-) + \quad [2.1]$$

$$= + (-) + \quad [2.2]$$

$$= + (-) + \quad [2.3]$$

Back EMF equations of each phase

$$= (\Theta) \quad [2.4]$$

$$= (\Theta - \frac{2}{3}) \quad [2.5]$$

$$= (\Theta - \frac{4}{3}) \quad [2.6]$$

Torque equations of each phase

$$= (\Theta) \quad [2.7]$$

$$= (\Theta - \frac{2}{3}) \quad [2.8]$$

$$= (\Theta - \frac{4}{3}) \quad [2.9]$$

Electromagnetic Torque equation

$$= + + \quad [2.1]$$

$$- = \frac{^2\Theta}{^2} + \frac{\Theta}{} \quad [2.1]$$

Electrical rotor angle

$$\Theta = \frac{}{2}\Theta \quad [2.1]$$

Angular rotor speed

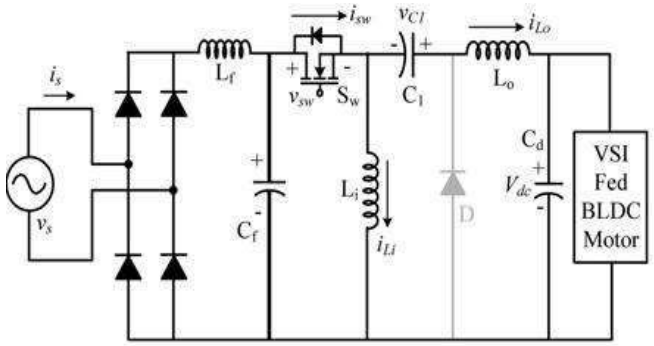
$$= \frac{\Theta}{} \quad [2.13]$$

3. ZETA CONVERTER

Analysis of the zeta converter operating in discontinuous conduction mode is used for power factor correction. The main advantage of zeta converter is that it is a naturally isolated structure, which allows a regulated output voltage with only one power processing stage. With the exception of the zeta converter all others (buck, boost, buck-boost, cuk and sepic) have been employed to correct the power factor of power supplies. However they have their intrinsic limitations.

OPERATION OF ZETA CONVERTER:

The zeta converter is operated in Discontinuous inductor current mode (DICM) to achieve wide range of speed control and power factor correction.



Mode I :

In this mode1, switch S_w is ON position. When inductors L_i and L_o are getting charged, the intermediate capacitor C_1 is discharged . The dc link capacitor C_d gets charged. In mode1, diode D is in off condition because positive polarity of the capacitor V_{c1} is applied to diode. When the diode D is said to be in OFF condition a voltage applied to capacitor is V_{c1}.

Mode II :

In mode 2, switch S_w is OFF position. So there is no supply to the remaining part of the circuit. In mode 2, inductor L_i and Lo are discharged. the diode D starts conducting. Hence the voltage across C_1 and V_{dc} gradually increases.

Mode III:

In mode 3, the switch S_w is in OFF position and this mode is said to be in discontinuous mode of operation. The inductor Li completely discharges and the inductor current becomes zero. When the inductor current becomes zero, the capacitor V_{dc} acts as source for VSI which is connected to BLDC. By this way voltage V_{dc} also decreases in this mode.

4.PROPOSED TOPOLOGY FOR BLDC MOTOR DRIVE SYSTEM

The complete operation of BLDC motor drive is realized using a single voltage sensor. A electronic commutation of BLDC motor is utilized for reducing the switching losses.[2]

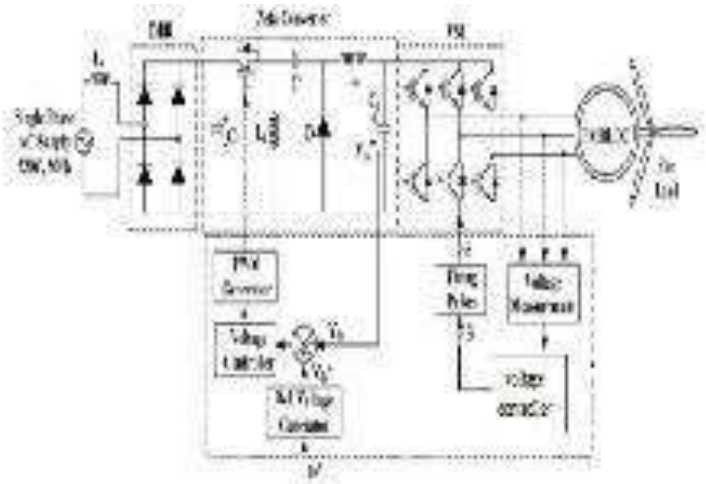


Fig 4.1 Proposed PFC zeta converter fed BLDC motor drive

Design of PFC zeta converter:

In switching period, the current flowing through inductor is discontinuous.

The duty ratio D for the zeta converter is

$$= \frac{\qquad}{\qquad} + \qquad [4.1]$$

The permitted ripple current in input inductor L_i and output inductor L_o the inductor value L_i and L_o are given as

$L_i = D.V_{in} / 2I_{in}f_s$ [4.2]

$L_0 = (1-D)V_{dc} / \{f_s.(\Delta i_{Lo})\}$ [4.3]

Where f_s is the switching frequency

The value of intermediate capacitor C_1 is

$C_1 = D.I_{dc} / \{f_s.(\Delta V_{C1})\}$ [4.4]

Here ΔV_{C1} is the permitted ripple in C_1.

The value of DC link capacitor C_d is written as

$C_d = I_{dc} / (2.\omega.\Delta V_{dc})$ [4.5]

Design of dc filter (L_f and C_f):

The filter capacitance at maximum is

[4.6]

The additional value of inductance required is

[4.7]

Control of proposed BLDC motor drive:

The front-end PFC converter is controlled by approach called voltage follower and the converter generates the PWM pulses for the PFC converter switch for dc-link voltage control. The PFC zeta converter is operated in DICM by single voltage control loop.

Reference dc-link voltage *,

$^*, = k_v \ ^*$ [4.8]

Generated voltage error signal,

$( ) = ^* \ ( ) - ( )$ [4.9]

Voltage error signal is generated by comparing reference dc-link voltage(*) with sensed dc-link voltage(V_{dc})
Generated output voltage(V_{cc}),

$( ) = \ (- 1) + \ \{ ( ) - \ (- 1)\} + \ ( )$ [4.10]

Controlled output voltage is generated by giving error voltage signal to PI controller

The PWM pulses is generated by comparing the output of voltage controller with a high-frequency saw-tooth signal(m_d)

$$\begin{cases} ( ) < ( ), \text{ then } s_w = \text{on} \\ ( ) \geq ( ), \text{ then } s_w = \text{off} \end{cases}$$ [4.11]

5. MATLAB SIMULATION CIRCUIT FOR BLDC MOTOR WITH ZETA CONVERTER

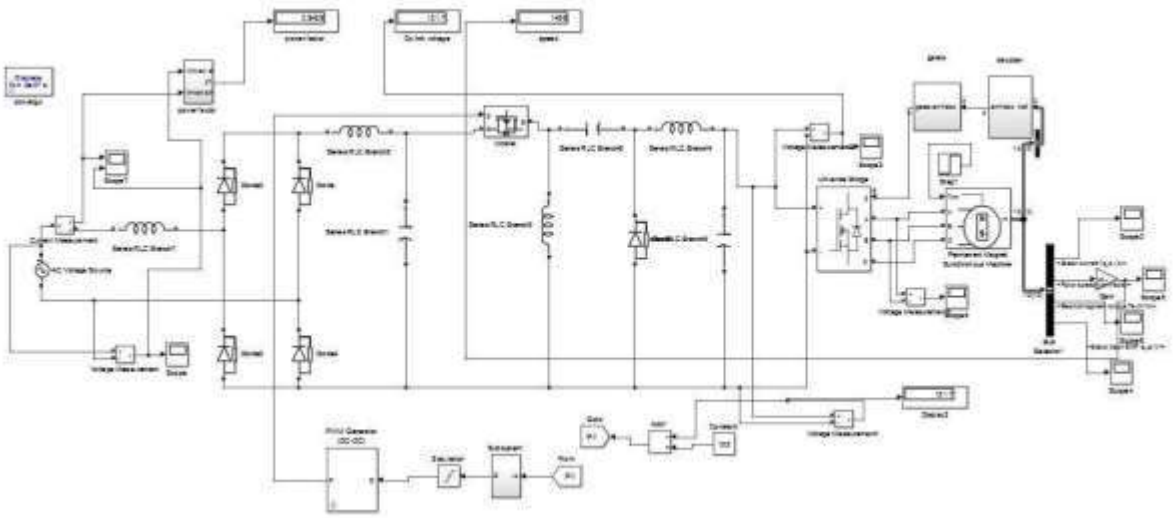
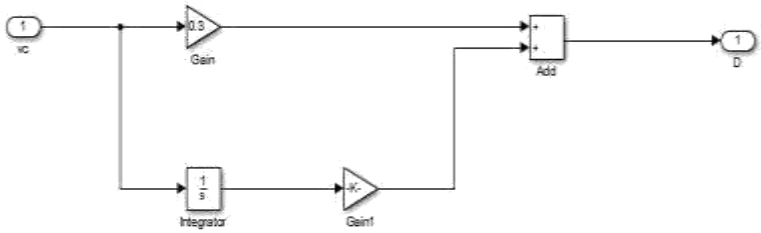


Figure:5.1 MATLAB Simulation circuit for BLDC motor with zeta converter



6. RESULT ANALYSIS

Simulation results were obtained using the proposed strategy to the BLDC motor has the following constants and ratings

Four pole, V_{rated} =200 V, R_{ph}(phase resistance) = 0.7Ω , L_{ph}(phase inductance) = 2.72mH, K_b(back emf) =88v/Krpm , Torque constant(K_t)=0.84Nm/A J(moment of inertia) =0.8Kg-m^2

Controller gains: K_p = 0.3 and K_i =0.001

Zeta converter parameters: input inductor (L_i)=1000μH, Intermediate capacitor (C_1)=400 nF,

Output Inductor (L_o)=3mH, DC link capacitor=2000μF,

DC filter inductance(L_f)=1.21mH,

DC filter capacitance (C_f) =330nF.

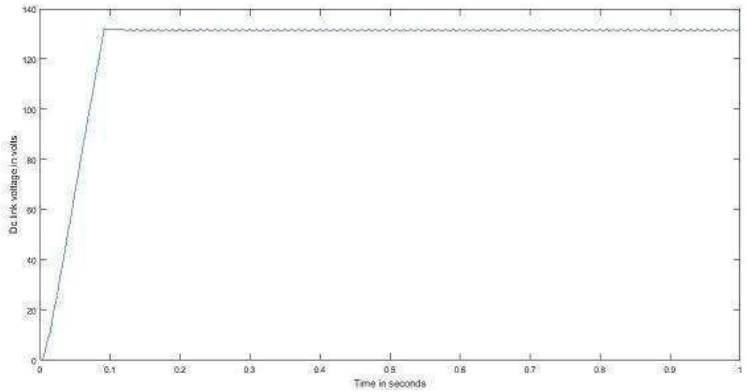


Fig:6.1 DC link voltage for input voltage v_s is 220v

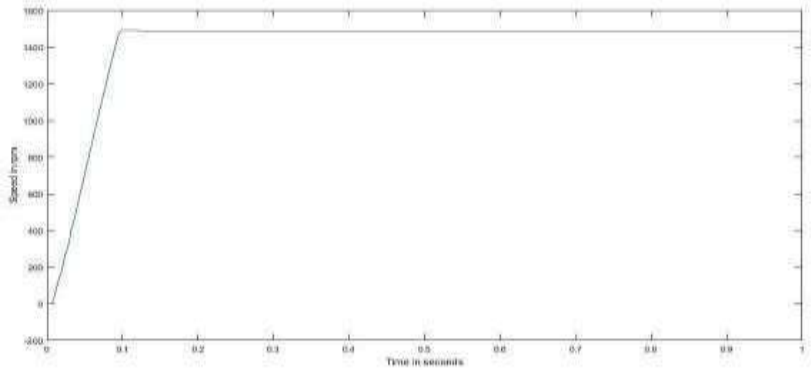


Fig:6.2 Speed of BLDC motor when operating at 220v

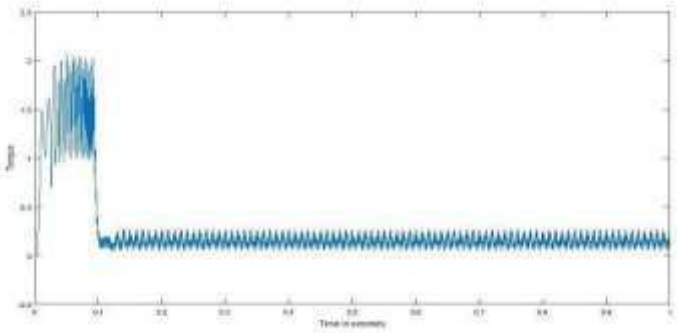


Fig: 6.3 Electromagnetic Torque (T_e) of BLDC motor when operating at 220v

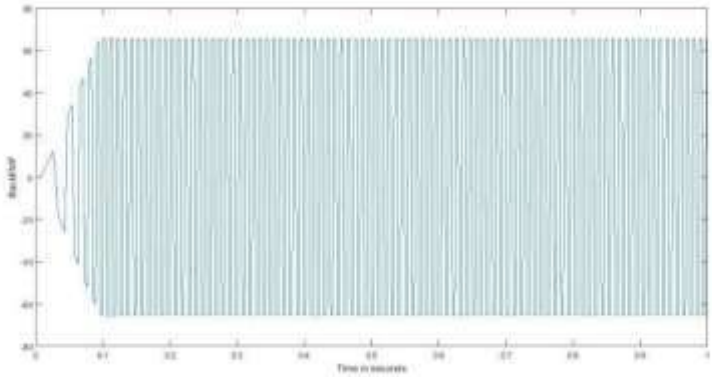


Fig:6.4 Back emf (E_b) of BLDC motor when operating at 220v

Simulated Results for various speeds and DC link voltages

DC link voltage (V_{dc})	Actual speed of the motor(Nr) in rpm	Power factor(cosφ)
131.5	1486	0.958
122.8	1388	0.969
114	1289	0.962
105.3	1189	0.944
96.3	1090	0.921
87.7	991	0.900

CONCLUSION

A Simple control using a voltage follower approach has been used for voltage control and power factor correction of a PFC zeta converter fed BLDC motor drive. The speed of BLDC motor has been controlled by varying the dc-link voltage of VSI via the PFC zeta converter.The PFC zeta converter has been designed to operate in DICM, which required a voltage follower for dc-link voltage control. The PFC zeta converter fed BLDC motor drive has been simulated and results were obtained. The zeta converter is found to be effective in improving the power factor of the BLDC motor drive. The proposed drive system has been found suitable to various adjustable speed drives for many low power applications.

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